

Flow Scalability per Broadcom Chipset

The maximum entries of OpenFlow Flows is often a critical piece of any SDN solution. This is a summary of Openflow Flow scalability per family of Chipset.

Chipset	ICAP (maximum entries in default or match modes)					ECAP (entries)	VCAP (entries)	L2 (maximum entries when buffer-mode is 5)	L3 (maximum entries when buffer-mode 5)			
	default	IPv4	IPv6	MAC	ARP				IPv4 host route	IPv4 forwarding route	IPv6 host route	IPv6 forwarding route
Helix4	2048	4096	2048	4096	2048	512	1024	32768	16384	6000	8192	3000
Firebolt4	2048	4096	2048	4096	4096	512	1024	32768	13100	4800	6550	2400
Trident+	1024	2048	1024	2048	2048	512	512	32768	12000	12000	6000	6000
Trident2	2048	4096	2048	4096	4096	512	512	32768	12000	12000	6000	6000
Trident2+	8192	16384	8192	16384	16384	512	512	32768	12000	12000	6000	6000
Trident3	3072	3072	3072	3072	3072	1024	512	32768	12000	12000	6000	6000
Tomahawk	1024	3072	1024	3072	3072	512	512	8192	6500	12000	3250	6000
Tomahawk2	1024	3072	1024	3072	3072	512	512	8192	6500	12000	3250	6000
Maverick	4096	8192	4096	8192	8192	512	512	32768	4000	12000	2000	6000